

Figure 1: Pinhole with 2 silicon apertures

Figure 2:

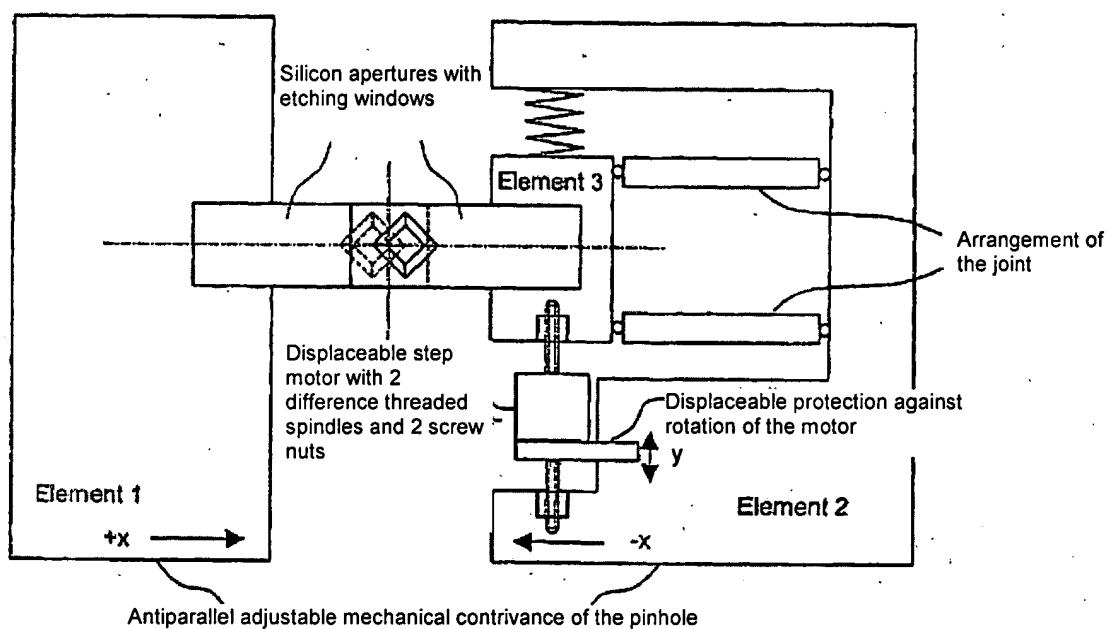


Figure 3b

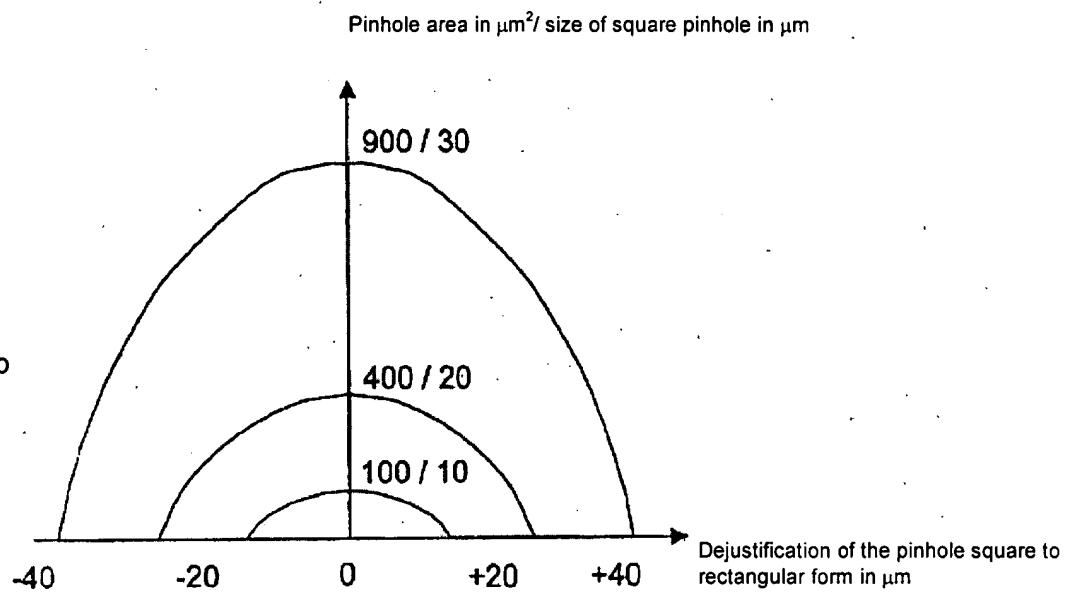


Figure 3a:

